Docket No.

248536US2RD DIV

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Hiroshi TSURUMI, et al.

SERIAL NO: NEW DIVISIONAL APPLICATION

GAU:

FILED:

**HEREWITH** 

**EXAMINER:** 

FOR:

RECEIVER

# INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

### REFERENCES

- The applicant(s) wishes to make of record the references listed on the attached PTO-1449 form. Copies of the listed references were made of record in parent application serial no. 09/604,838, filed June 27, 2000, as were either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

#### RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

## CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

## **DEPOSIT ACCOUNT**

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,

MAJER & NEUSTADT, P.C. Unester

Eckhard H. Kuesters

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Form PTO 1449 (Modified)		U.S. DEPARTMENT PATENT AND TRAD		ATTY DOCKET NO.		SERIAL NO.  New Divisional Application		
(Modified)		FATERI AND ITAL	ZWARR OFFICE	248536US2RD DIV				
LIST OF REFERENCES CITED BY APPLICANT			APPLICANT Hiroshi TSURUMI, et al.					
				Herewith				
				U.S. PATENT DOCUMENTS	_			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA	6,545,516 B2	04/2003	Ylamurto et al				
	AB	6,297,637 B1	10/2001	Feld et al				
	AC	6,337,885 B1	01/2002	Hellberg				
	AD	6,334,051 B1	12/2001	Tsurumi et al				
	AE							
	AF							
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		DOCUMENT NUMBER	DATE	COUNTRY		TRANSLATION YES NO		
	AD							
	AP							
	AQ							
	AR			TW - 14 1 4 - 74				
	AS							
	AT							
		OTHER RE	FERENCES (	' Including Author, Title, Date, Pertinen	t Pages, e	tc.)		
	AU	Sansen et al., "A 900-mV Low-Power DS A/D COnverter with 77 dB Dynamic Range", IEEE Journal of Solid-State Circuits, vol. 33, no. 12, pp. 1887-1897.						
<u></u>	AV	Fong et al., "Monolithic RF Active Mixer Design", IEEE 1999 Transactions on Circuits and Systems - II: Analog and Digital Signal Processing, vol. 46, no. 3, pp. 231-239.						
-	AW	P.R. GRAY, et al., "FUTURE DIRECTIONS IN SILICON ICS FOR RF PERSONAL COMMUNICATIONS," IEEE 1995 Custom Integrated Circuits Conference, 1995, pgs. 83-90.						
	AX	Jan CROLS, et al., "A SINGLE CHIP 900 MHZ CMOS RECEIVER FRONT-END W ITH A HIGH PERFORMANCE LOW-IF TOPOLOGY," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pgs. 1483-1492.						
	AY	Jan CROLS, et al., "LOW-IF TOPOLOGIES FOR HIGH PERFORMANCE ANALOG FROM ENDS OF FULLY INTEGRATED RECEIVERS," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pgs. 269-282.						
Examiner Date Considered								
				t citation is in conformance with MPEP 60	09; Draw li	ne through	citation	if not in